

**AMENDMENTS TO THE SPECIFICATION**

The amendments here conform the drawings and specification.

At page 18, please amend paragraph [0063], line 6 as follows:

Downstream elements in FIG. 6A include the IP formatter 118 and physical layer (e.g., Ethernet) formatter 120. Alternatively TCP formatter 112 is replaced by a User Datagram Protocol (UDP) formatter, and Ethernet formatter 120 is replaced by a formatter adapted for wireless Ethernet, USB (universal serial bus) or IEEE 1394 standard communications. Formatter 120 is conventionally connected via the Internet and/or a home network 124 at the receiving end to Ethernet deformatter 130, IP deformatter 128, IP deformatter 130, and TCP deformatter 132. All elements 110 to 132 are conventional.

At page 18, please amend paragraph [0064], line 5 as follows:

FIGS. 6B to 6D show variants of the FIG. 6A system, for various control uses of the APS encoding method, with like elements similarly labeled. FIG. 6B shows the system configured for control of rendering uses where the output video at the receiving end (lower right of figure) is composite/component analog video. In this case resolution video reduction circuit 154 [132] is coupled upstream of video encoder 66 to provide a particular image resolution or quality level, in terms of number of pixels.

At page 18, please amend paragraph [0065], line 3 as follows:

FIG. 6C is similar to FIG. 6B except that at the output there is provided computer-type analog component video of the VGA, XVGA, or SVGA type. As in FIG. 6B, there is resolution reduction circuit 154, [132,] here connected to RGB video formatter 136 driving four digital to analog converters 138, ..., 144 providing red (R), green (G), blue (B) and synchronization (sync) analog video output signals.